

IGAADC13A

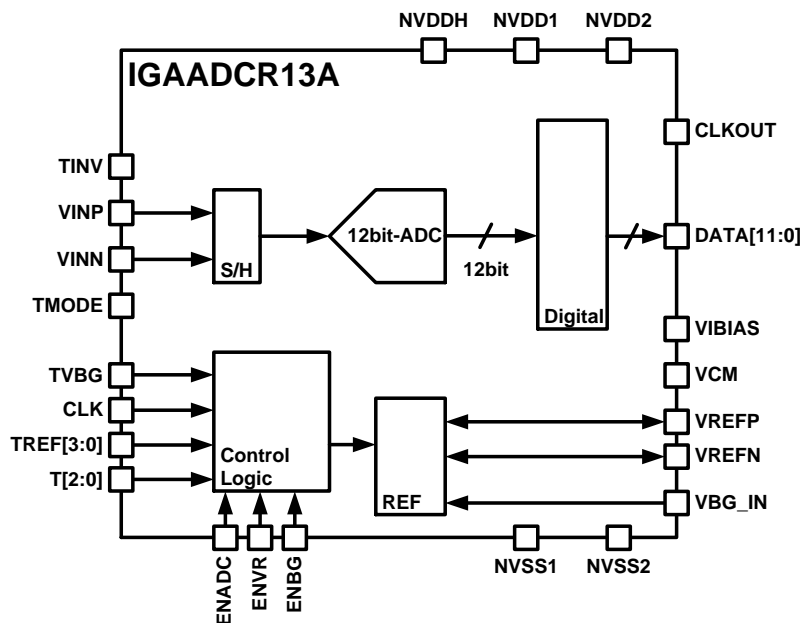
TSMC CLN55LP 12Bit 100MHz Pipelined ADC [1ch]

Overview

IGAADC13A is a general-purpose analog to digital converter (ADC) with 12-bit resolution. This ADC is based on pipelined architecture and the sampling rate can be up to 100MHz.

IGAADC13A contains one sample/hold circuit and one 12-bit ADC and high speed parallel interface.

A power down mode is available to disable IGAADC13A and is used for lower power dissipation.



IGAADC13A Block Diagram

Features

- 12-bit resolution
- 100MHz conversion rate
- Dual supply voltage, 3.3V and 1.2V for digital
- Differential input is available
- 2's complement format
- Built in analog input inverse function
- Power down mode is available
- Operation temperature : -40°C to 125°C

Technology

- TSMC 55nm 1.2V/2.5V Low Power Process
- Metal scheme :4X1Z
- Device information
Core device : nch / pch
IO device : nch_25 / pch_25
High R resistor: rppolywo
BJT : PNP

Applications

- Pipelined ADC

Deliverables

Item	Description	Format
1	Comprehensive Documents	.pdf
2	Hard Macro	.gds / .spi
3	Synthesis Model	.lib / .db
4	Verilog Model	.v
5	LEF Model	.lef

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